

## NP50P06SDG-VB Datasheet

### P-Channel 60 V (D-S) MOSFET

#### PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
- 60	0.020 at V <sub>GS</sub> = - 10 V	- 50
	0.025 at V <sub>GS</sub> = - 4.5 V	- 45

#### FEATURES

- TrenchFET<sup>®</sup> Power MOSFET
- Material categorization:

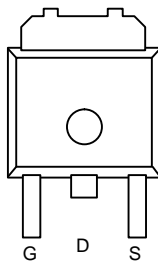


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

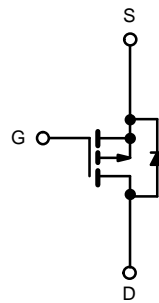
#### APPLICATIONS

- Load Switch

TO-252



Top View



P-Channel MOSFET

#### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	- 60	V	
Gate-Source Voltage	V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 175 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	- 50	
		T <sub>C</sub> = 125 °C	- 40	
Pulsed Drain Current	I <sub>DM</sub>	- 160	A	
Avalanche Current	I <sub>AS</sub>	- 50		
Single Pulse Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	125	mJ
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	113 <sup>c</sup>	W
		T <sub>A</sub> = 25 °C	2.5 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit	
Junction-to-Ambient <sup>b</sup>	R <sub>thJA</sub>	t ≤ 10 s	15	18	°C/W
		Steady State	40	50	
Junction-to-Case	R <sub>thJC</sub>	0.82	1.1		

Notes:

- Duty cycle ≤ 1 %.
- When mounted on 1" square PCB (FR-4 material).
- See SOA curve for voltage derating.
- Package limited.

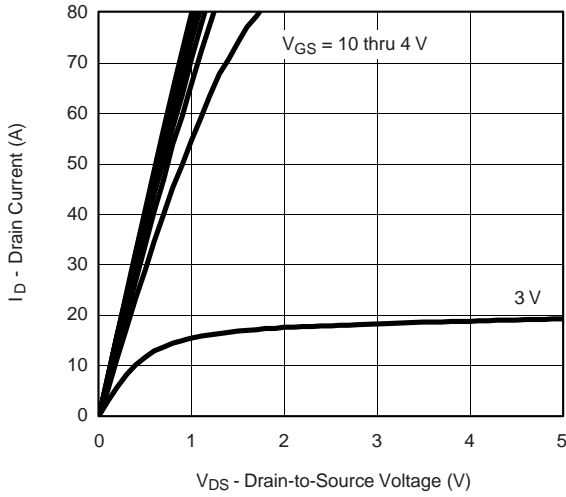
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	- 60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 1.5		- 3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}$			- 1	$\mu\text{A}$
		$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			- 50	
		$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 150\text{ }^\circ\text{C}$			- 100	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -10\text{ V}$	- 50			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -17\text{ A}$		0.020		$\Omega$
		$V_{GS} = -10\text{ V}, I_D = -40\text{ A}, T_J = 125\text{ }^\circ\text{C}$		0.030		
		$V_{GS} = -10\text{ V}, I_D = -40\text{ A}, T_J = 150\text{ }^\circ\text{C}$		0.035		
		$V_{GS} = -4.5\text{ V}, I_D = -14\text{ A}$		0.025		
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15\text{ V}, I_D = -17\text{ A}$		61		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = -25\text{ V}, f = 1\text{ MHz}$		2950		$\mu\text{F}$
Output Capacitance	$C_{oss}$			380		
Reverse Transfer Capacitance	$C_{rss}$			305		
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = -30\text{ V}, V_{GS} = -10\text{ V}, I_D = -40\text{ A}$		110	165	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			19		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			28		
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = -30\text{ V}, R_L = 0.6\text{ }\Omega$ $I_D \cong -40\text{ A}, V_{GEN} = -10\text{ V}, R_G = 6\text{ }\Omega$		15	23	ns
Rise Time <sup>c</sup>	$t_r$			70	105	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			175	260	
Fall Time <sup>c</sup>	$t_f$			175	260	
<b>Source-Drain Diode Ratings and Characteristics</b> $T_C = 25\text{ }^\circ\text{C}^b$						
Continuous Current	$I_S$				- 40	A
Pulsed Current	$I_{SM}$				- 80	
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = -40\text{ A}, V_{GS} = 0\text{ V}$		- 1	- 1.6	V
Reverse Recovery Time	$t_{rr}$	$I_F = -40\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}$		45	70	ns

## Notes:

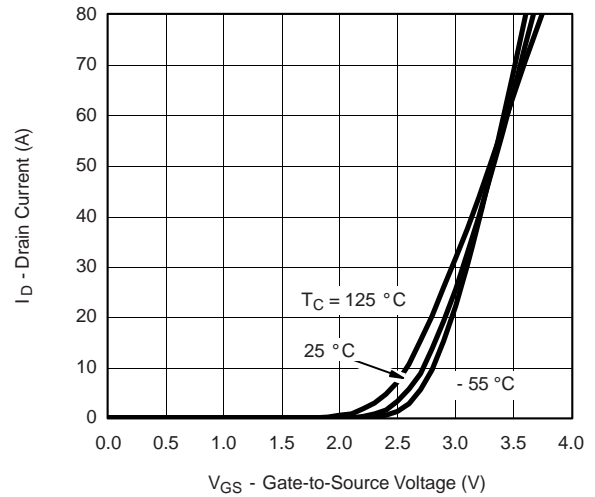
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

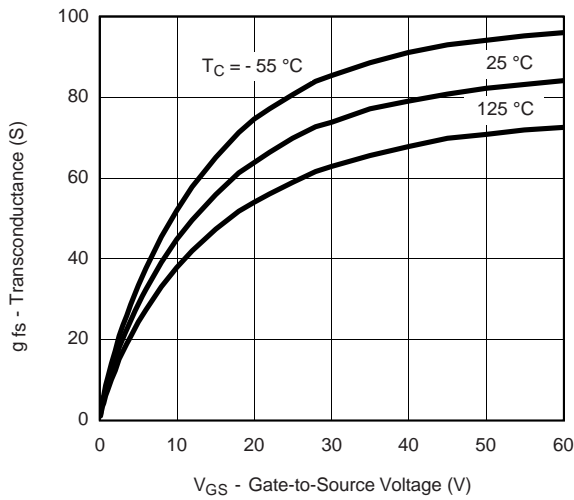
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



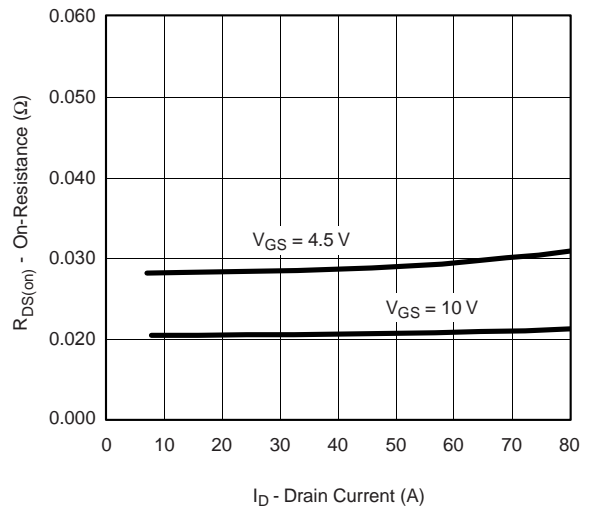
**Output Characteristics**



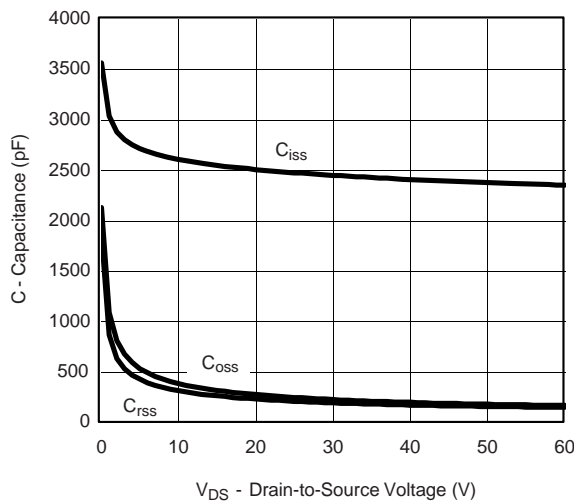
**Transfer Characteristics**



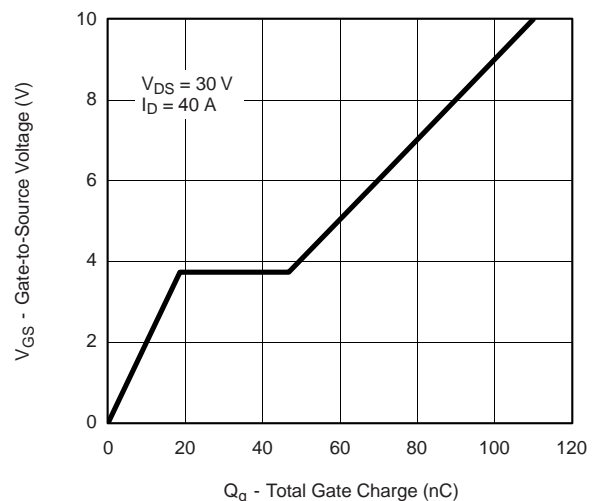
**Transconductance**



**On-Resistance vs. Drain Current**

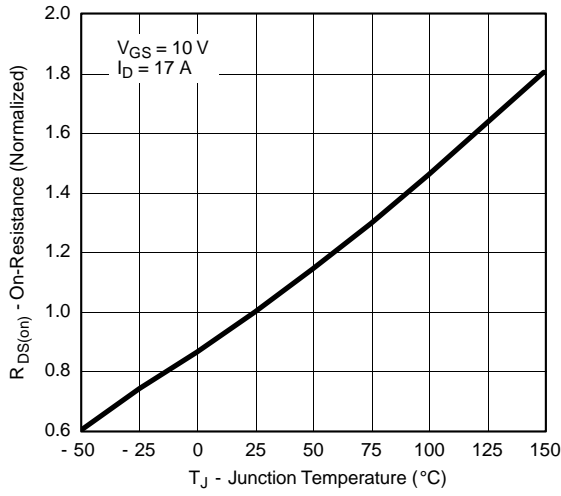


**Capacitance**

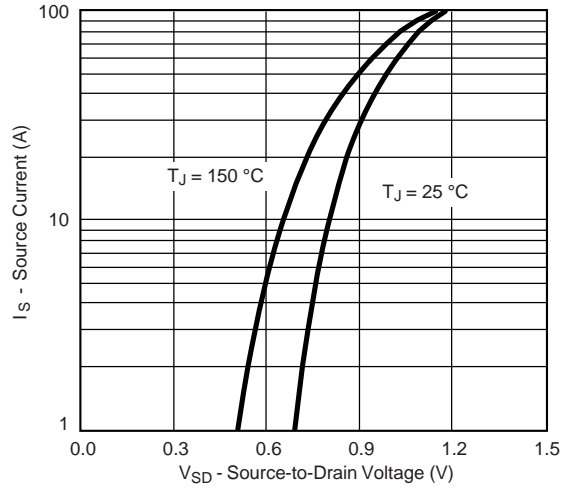


**Gate Charge**

**TYPICAL CHARACTERISTICS**

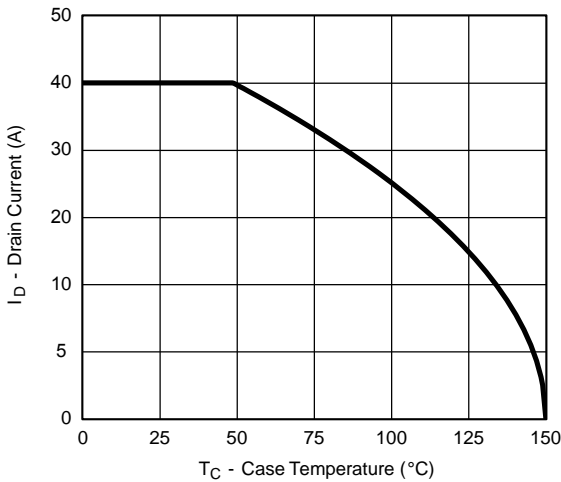


On-Resistance vs. Junction Temperature

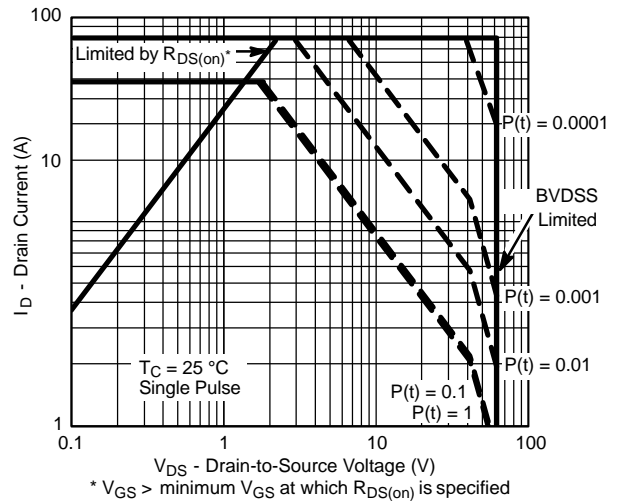


Source-Drain Diode Forward Voltage

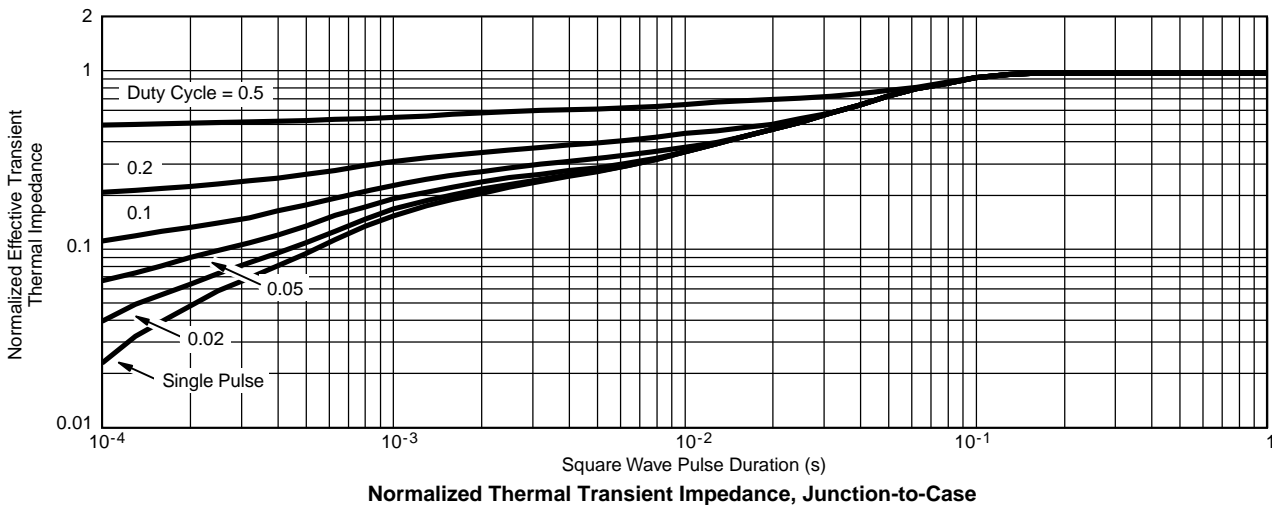
**THERMAL RATINGS (25 °C, unless otherwise noted)**



Drain Current vs. Case Temperature

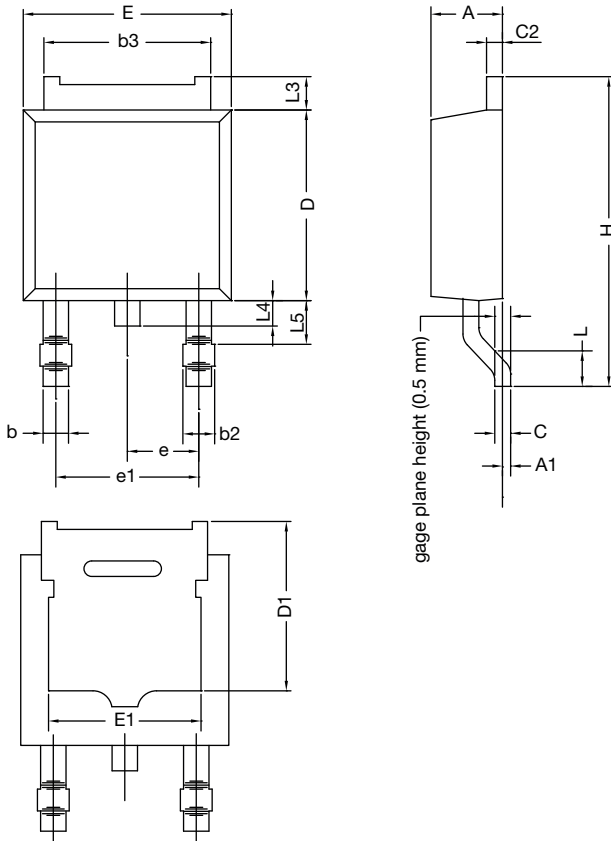


Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

## TO-252AA CASE OUTLINE

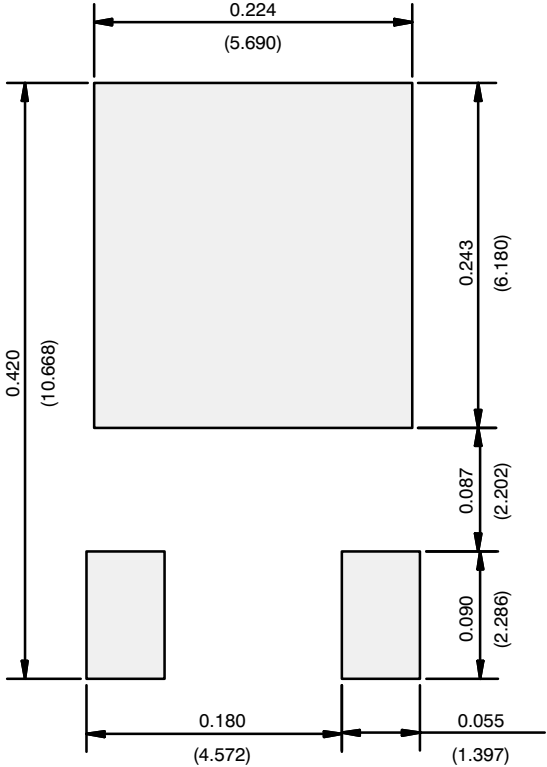


DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
C	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	5.21	-	0.205	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
H	9.40	10.41	0.370	0.410
e	2.28 BSC		0.090 BSC	
e1	4.56 BSC		0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.14	1.52	0.045	0.060
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347				

**Note**

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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